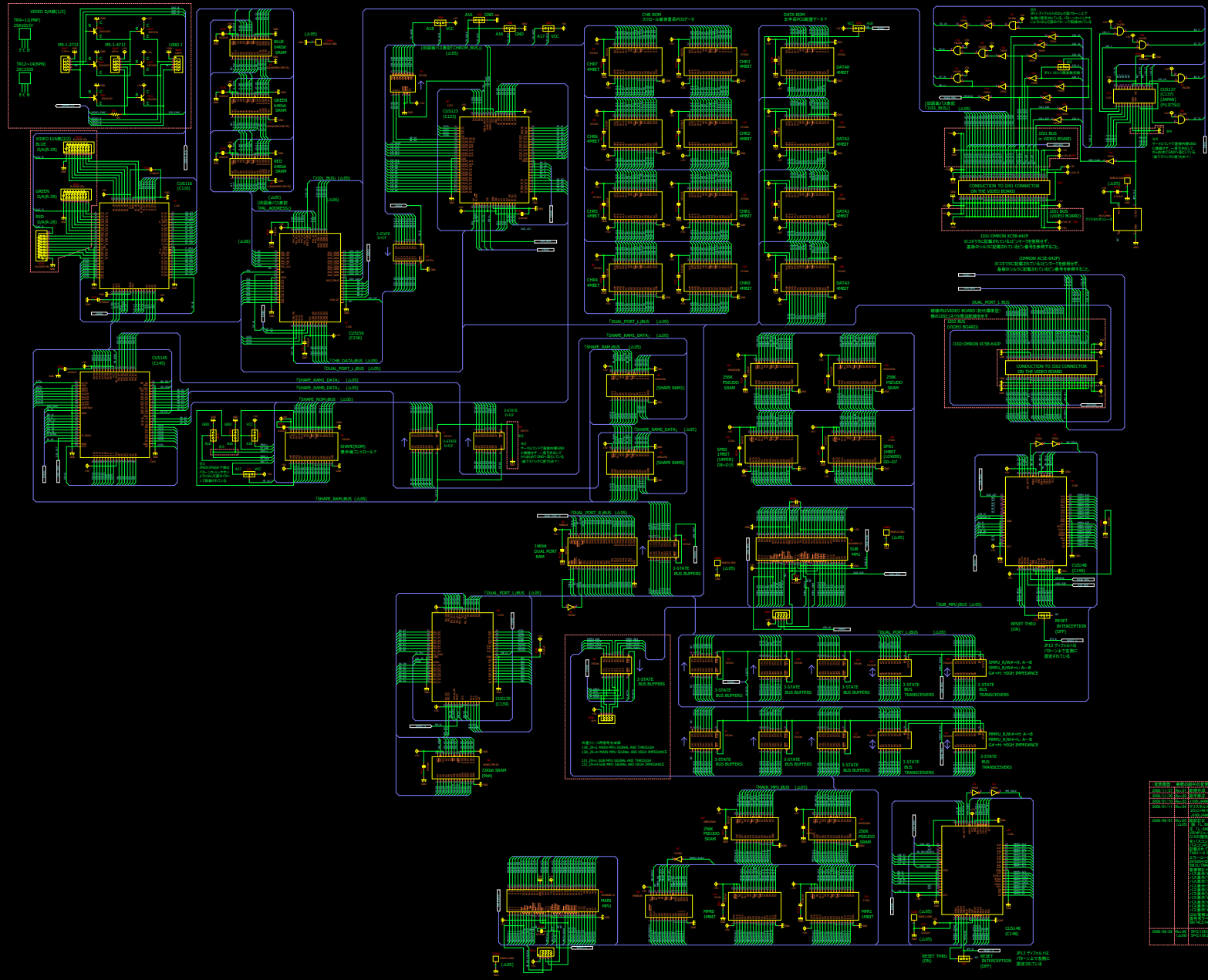
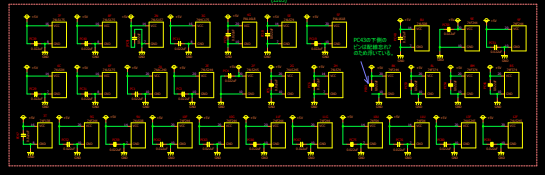
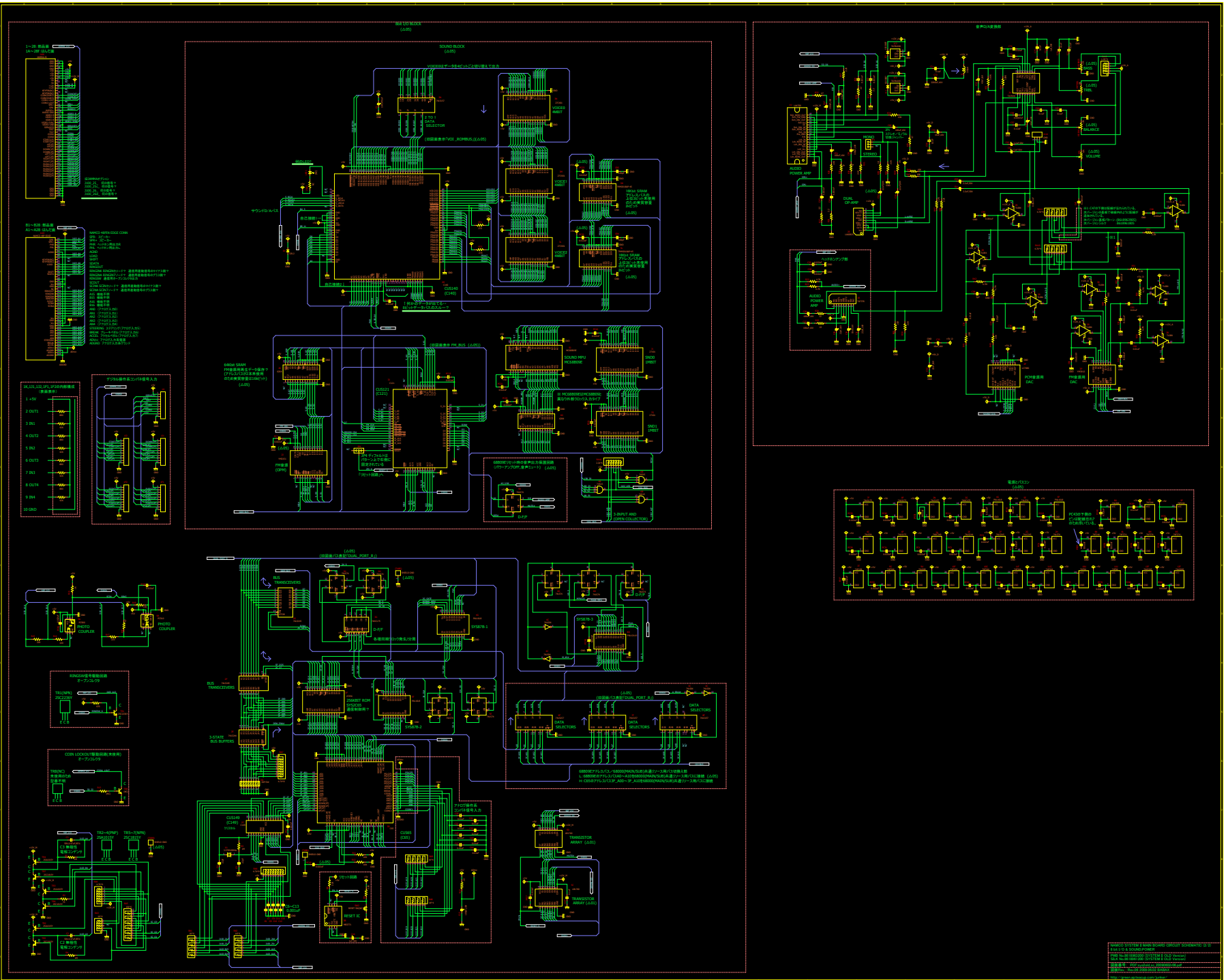


IBM LO BLOCK



REV	DATE	DESCRIPTION
1.0	1984.03.15	INITIAL DESIGN
1.1	1984.04.01	REVISED POWER SUPPLY
1.2	1984.04.15	REVISED MEMORY ADDRESS DECODING
1.3	1984.05.01	REVISED CPU ADDRESS DECODING
1.4	1984.05.15	REVISED I/O ADDRESS DECODING
1.5	1984.06.01	REVISED VIDEO SIGNALS
1.6	1984.06.15	REVISED POWER SUPPLY
1.7	1984.07.01	REVISED MEMORY ADDRESS DECODING
1.8	1984.07.15	REVISED CPU ADDRESS DECODING
1.9	1984.08.01	REVISED I/O ADDRESS DECODING
1.10	1984.08.15	REVISED VIDEO SIGNALS
1.11	1984.09.01	REVISED POWER SUPPLY
1.12	1984.09.15	REVISED MEMORY ADDRESS DECODING
1.13	1984.10.01	REVISED CPU ADDRESS DECODING
1.14	1984.10.15	REVISED I/O ADDRESS DECODING
1.15	1984.11.01	REVISED VIDEO SIGNALS
1.16	1984.11.15	REVISED POWER SUPPLY
1.17	1984.12.01	REVISED MEMORY ADDRESS DECODING
1.18	1984.12.15	REVISED CPU ADDRESS DECODING
1.19	1985.01.01	REVISED I/O ADDRESS DECODING
1.20	1985.01.15	REVISED VIDEO SIGNALS

IBM LO BLOCK
PCB LAYOUT
REV 1.0



Component	Value
1. 1KΩ	1.0K
2. 10KΩ	10.0K
3. 100KΩ	100.0K
4. 1MΩ	1.0M
5. 10MΩ	10.0M
6. 100MΩ	100.0M
7. 1GΩ	1.0G
8. 10GΩ	10.0G
9. 100GΩ	100.0G
10. 1TΩ	1.0T